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CONFIRMATION NO. 4569

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| 10/678,055 | 10/02/2003 RULE | 363 | 2838 | 42P6963R |

APPLICANTS
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**** CONTINUING DATA *******
 This application is a REI of 09/405,977 09/27/1999 PAT 6,297,974

**** FOREIGN APPLICATIONS *******

**** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ****
 06/03/2004

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|--|--|--|---------------------------|-------------------------|-----------------------|----------------------------|
| Foreign Priority claimed 35 USC 119(a-d) conditions met Verified and Acknowledged | <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No /RAJNIKANT B PATEL/ Examiner's Signature | <input type="checkbox"/> Met after Allowance Initials | STATE OR COUNTRY CA | SHEETS DRAWINGS 7 | TOTAL CLAIMS 37 | INDEPENDENT CLAIMS 7 |
|--|--|--|---------------------------|-------------------------|-----------------------|----------------------------|

ADDRESS
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TITLE
 Method and apparatus for reducing stress across capacitors used in integrated circuits

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